

e-Diagnostics & EEC Workshop - 20 July 2001

What: This workshop is an e-Diagnostics update and Equipment Engineering Capability introduction . A summary of requirements and implementation guidelines for e-Diagnostics & EEC will be provided. This meeting provides an opportunity to widely distribute the material to encourage consistent industry implementations.

Who: Any interested chip manufacturers and suppliers

Why: To update interested IC Makers & equipment & software suppliers on the requirements for e-Diagnostics and EEC.

Where: San Francisco Marriott

Expected Results: Further alignment of chip manufacturer and supplier implementation strategies for e-Diagnostics/EEC and a common understanding of issues.

www.sematech.org/public/resources/ediag/index.htm

e-Diagnostics & EEC Workshop Agenda

08:30 <u>IC Maker “e”-Requirements</u>	B. Crandell T. Masui
09:00 <u>e-Diagnostics Summary</u>	H. Wohlwend
09:30 <u>e-Diagnostics Security Requirements</u>	A. Raman
09:45 <u>e-Diagnostics Measurement & Assessment</u>	C. Stambaugh
10:00 Break	
10:20 <u>EEC Guidelines</u>	B. Van Eck D. Bloss G. Inoue
11:00 <u>EEC Status Report</u>	S. Kobayashi
11:15 Panel, Q & A	
11:50 <u>Summary and Next Steps</u>	H. Wohlwend
12:00 Adjourn	