

e-Diagnostics & EEC Workshop

Austin, Texas

19 October 2001



e-Diagnostics & EEC Workshop - 19 Oct. 2001

What: This workshop is an e-Diagnostics and Equipment Engineering Capability update. A summary of requirements and implementation guidelines for e-Diagnostics & EEC will be provided. This meeting provides an opportunity to widely distribute the material to encourage consistent industry implementations.

Who: Any interested chip manufacturers and suppliers

Why: To update interested IC Makers & equipment & software suppliers on the requirements for e-Diagnostics and EEC.

Where: Capitol/Austin Marriott, Yerba Buena Salon

Expected Results: Further alignment of chip manufacturer and supplier implementation strategies for e-Diagnostics/EEC and a common understanding of issues.

www.sematech.org/public/resources/ediag/index.htm

e-Diagnostics & EEC Workshop Agenda

Agenda

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| 08:30 IC Maker “e”-Requirements | D. Bloss |
| 09:00 e-Diagnostics to EEC to e-Manufacturing | H. Wohlwend |
| 09:25 e-Diagnostics Security Requirements | A. Raman |
| 09:45 e-Diagnostics Measurement & Assessment | C. Dewitt |
| 10:00 Break | |
| 10:20 EEC Guidelines | B. Crandell, J. Toth |
| 11:00 IEE Task Force | L. Rist |
| 11:15 Panel, Q & A | |
| 11:40 Summary and Next Steps | K. Gartland |
| 12:00 Adjourn | |

Open Conference Confidentiality Notice

- **This meeting is an OPEN CONFERENCE.**
- **Confidential or Proprietary information may NOT be disclosed.**
- **All meeting attendees are permitted to take notes or otherwise make a personal record of these proceedings.**
- **All presentations will be available to the public via the International SEMATECH public website following this meeting.**